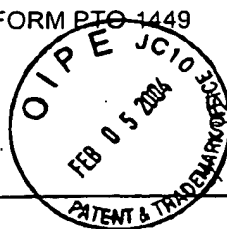


FORM PTO 1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
AL-256APPLN. NO.  
10/691,152APPLICANT  
Gregory StarrCONF. NO.  
6613FILING DATE  
October 21, 2003GROUP ART UNIT  
2816INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	Re. 35,797	5/1998	Graham et al.	326	40	
	3,473,160	10/1969	Wahlstrom	326	41	
	4,494,021	1/1985	Bell et al.	307	591	
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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	0 266 065	5/1988	European Pat. Off.				
<i>ind</i>	0 416 930	3/1991	European Pat. Off.			<input checked="" type="checkbox"/>	
	0 778 517	6/1997	European Pat. Off.				
	0 987 822	3/2000	European Pat. Off.				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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	Advanced Micro Devices, Inc., "Am2971 Programmable Event Generator (PEG)," Publication No. 05280, Rev. C, Amendment /0, pp. 4-286 - 4-303 (July 1986)
	Advanced Micro Devices, Inc., "AmPAL*23S8 20-Pin IMOX PAL-Based Sequencer," Publication No. 06207, Rev. B, Amendment /0, pp. 4-102 - 4-121 (October 1986)
	Agere Systems, Inc., "ORCA ORT82G5 0.622/1.0-1.25/2.0-2.5/3.125 Gbits/s Backplane Interface FPSC," Preliminary Data Sheet, pp. 1-35 (July 2001)
	Agere Systems, Inc., "ORCA 8850 Field-Programmable System Chip (FPSC) Eight Channel x 850 Mbits/s Backplane Transceiver," Product Brief, pp. 1-6 (July 2001)
	Agere Systems, Inc., "ORCA 8850 Field-Programmable System Chip (FPSC) Eight Channel x 850 Mbits/s Backplane Transceiver," Product Brief, pp. 1-36 (August 2001)

EXAMINER

DATE CONSIDERED 05/18/2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.

previously considered

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